Roll No.

PAPER ID-16003

B. Tech. EXAMINATION, 2024

(First Semester)

COMPUTER SCIENCE AND ENGINEERING (CYBER SECURITY)

Code: EEE-101

Basics of Electrical and Electronics Engineering

Time: 3 Hours

Maximum Marks: 70

Before answering the question-paper candidates should ensure that they have been supplied to correct and complete question-paper. No complaint, in this regard, will be entertained after the examination.

Note: Attempt Five questions in all, selecting one question from each Unit. Q. No. 1 is compulsory.

(a) Justify Electrical and electronics are different.

(b) State Kirchhoff's voltage and current law.

Draw the symbols:

2

(i) PNP Transistor

(ji) MOSFET

(c)

(iii) Current source

(iv) Wattmeter.

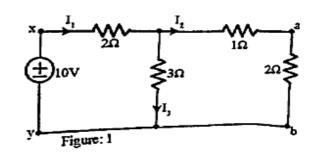
(9) What is a balanced system?

(e) Write Shockley equation. 2

Unit I

(a) Elaborate role and importance of circuits in Engineering.

(b) Calculate the total current in the circuit by applying suitable theorem. 15



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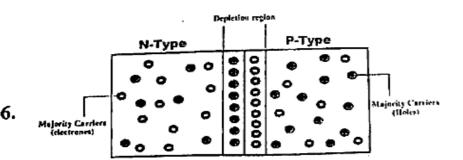
2

- Derive star to delta conversion. 3. (a)
 - Why source transformation is required? (b) State Thevenin and Norton theorem. 15

Upit II

- 4. Explain in detail active, Reactive, apparent 15 power, power factor and energy.
- With the help of sinusoidal waveform (a)analyse all AC parameters.
 - With the help of figure analyze the RLC (bY series and parallel circuit. 15

Unit III



Comment and critically analyse the above 15 diagram.

3

Draw the output characteristics of Common Emitter configuration. Write the effect of collector voltage on the collator with reference current characteristics. What is the effect of base current IB on collector current IC with reference to characteristics ?

Draw the circuit diagram of voltage divider biasing method of BJT. How stability in operating point is obtained?

Unit IV

- With the help of neat diagram write (a) construction of n-channel FET.
 - Explain the working of n-channel D-MOSFET. 15
- Explain and draw the characteristics of field effect transistor. 15

4

15

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